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**U.S. PATENT DOCUMENTS**

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**OTHER ART (Including Author, Title, Date, Pertinent Pages, Etc.)**

	XP007907512; "Application of the Genetic Algorithm to Compact MOSFET Model Development and Parameter Extraction" X. Cai*, H Wang*, X. Gu*, G.Gildenblat* and P. Bendix**; *Department of Electrical Engineering, The Pennsylvania State University, University Park PA 16802, USA, <a href="mailto:xqc100@psu.edu">xqc100@psu.edu</a> , *LSI Logic, 1551 McCarthy Blvd., MS J-100, Milpitas, CA 95035
	XP007907426; "Extraction of Compact Model Parameters for ULSI MOSFETs Using a Genetic Algorithm" Josef Watts*, Calvin Bittner*, Douglas Heaberlin*, and James Hoffmann**; *IBM Microelectronics, Essex Junction, VT, USA; **University of Vermont, Burlington, VT USA
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